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10/071298  
02/07/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10071298	02/07/2002	361	58	2836	BENENSON

\*\*APPLICANTS: Belgacem Haba;

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\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/564,064 05/03/2000

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	9797-0074-999
Verified and Acknowledged Examiners's initials		

TITLE : Semiconductor module with serial bus connection to multiple dies

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED:		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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